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FIGS. 26A-26B and 27A-27I are views for explaining procedures to probe and measure the semiconductor IC board fabricated according to the embodiments of the invention;

FIGS. 28A-28B are views for explaining steps of fabricating a probe electrode in a probing apparatus for use in the probing and measurement; and

FIGS. 29A-29E are views for explaining prior arts.

REMARKS

By this Amendment, the "Brief Description Of Drawings" section of the specification has been revised to correct an error (the description of Figs. 28 and 29 were duplicates) and to properly identify the sub-figures throughout the drawings.

Entry of this Preliminary Amendment is respectfully requested.

Respectfully submitted,

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irregularities, or concave or convex. As a result, when the tip end of the metal post is dipped in, for example, a solder bath or a plating bath, molten solder or plating liquid enters concaves of the irregularities, and hence surface tension occurs to solder or plating liquid therearound. Accordingly, the solder bump can be easily formed at the tip end of the metal post.

BRIEF DESCRIPTION OF DRAWINGS

FIGS. IA-IK are

FIG. 1 is views for explaining fabricating steps according to a first embodiment of the invention;

FIGS. 2A-ZB are FIG. 2 is views for explaining a case where a semiconductor IC board fabricated according to the first embodiment of the invention is connected to an external printed board;

FIGS. 3A-3B are FIG. 3 is views for explaining a case where the semiconductor IC board fabricated according to the first embodiment of the invention is filled with an insulating material;

FIG. 4 is a view for explaining a case where a guide mark is attached to the semiconductor IC board fabricated according to the first embodiment of the invention;

F165.5A-5B are FIG. 5 is views for explaining side views in FIG. 8;

FIG. 6-16 core FIG. 6-is views for explaining a case where heights of metal posts which are fabricated according to the first embodiment of the invention are varied;

FIGS. 7A-7K and 8A-8D FIGS. 7 and 8 are views for explaining fabricating steps according to a second embodiment of the invention;

FIGS. 9A-9I are FIG. 9 is views for explaining fabricating steps according to a third embodiment of the invention;

FIGs. 10 and 11 are views for explaining fabricating steps according

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to a fourth embodiment of the invention; FIGS. 12A-12C, 13A-13C, 14A-14B and 15A-15C FIGS. 12 to 15 are views for explaining fabricating steps according to a fifth embodiment of the invention; FIGS. 16A-16E and 17A-17B FIGs. 16 and 17, are views for explaining fabricating steps according to a sixth embodiment of the invention; F165.18A-18H are FIG. 18 is views for explaining fabricating steps according to a seventh embodiment of the invention; FIGS. 19A-19D are FIG. 19 is views for explaining fabricating steps according to an eighth embodiment of the invention; FIGS 200A - 20D are FIG. 20 is views for explaining fabricating steps according to a ninth embodiment of the invention; FIGS, 21A-21C and 22A-22D FIGs. 21 and 22 are views for explaining fabricating steps according to a tenth embodiment of the invention; FIGS. 23A-23D are FIG. 23 is views for explaining fabricating steps according to an eleventh embodiment of the invention; F165,24A-246 are FIG. 24 is views for explaining fabricating steps according to a twelfth embodiment of the invention; F165.25A-25C are FIG. 25 is views for explaining the construction according to a thirteenth embodiment of the invention; FIGS. 26A-26B and 27A-27I FIGs. 26 and 27 are views for explaining procedures to probe and measure the semiconductor IC board fabricated according to the

embodiments of the invention; F165. 28A-28B are

FIG. 28 is views for explaining steps of fabricating a probe electrode in a probing apparatus for use in the probing and measurement; and

FIG. 29 is views for explaining steps of fabricating a probe electrode in a probing device for use in the probing and measurement; and F165, 29A-29 Bare FIG. 30 is views for explaining prior arts.